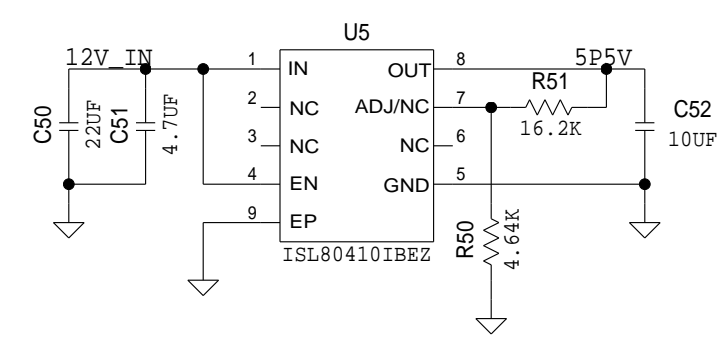
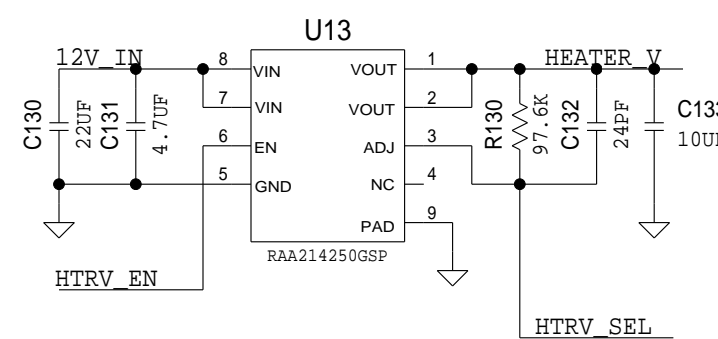
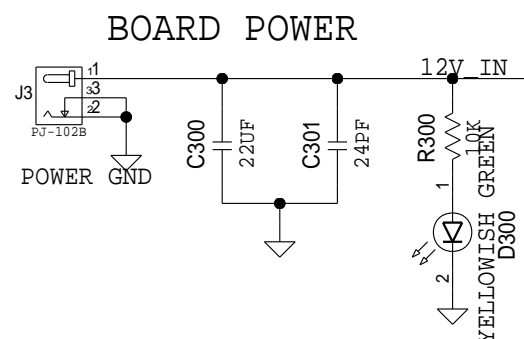
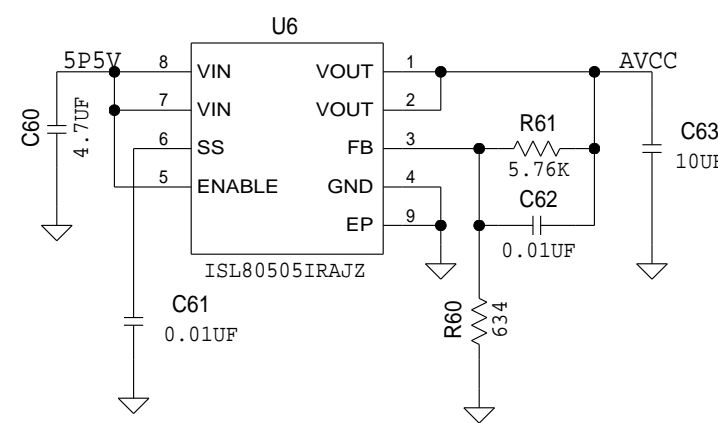


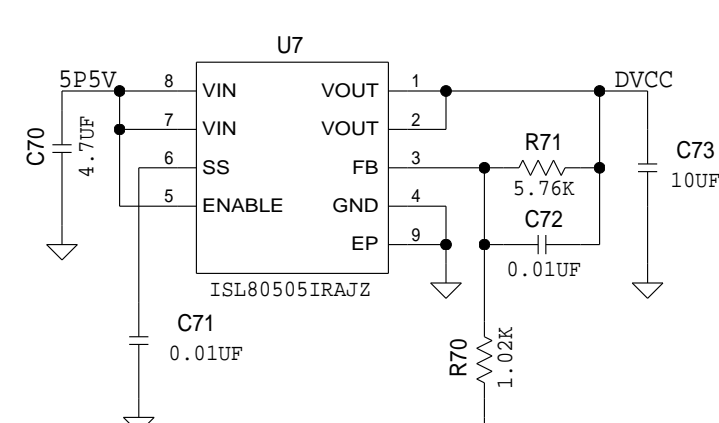
# POWER SUPPLY AND VREF CIRCUITRY



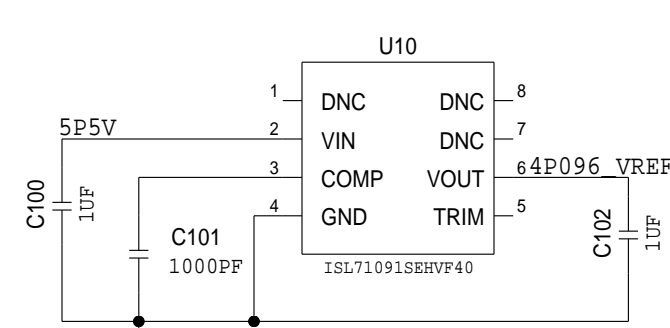
5.5V SUPPLY



5V AVCC SUPPLY



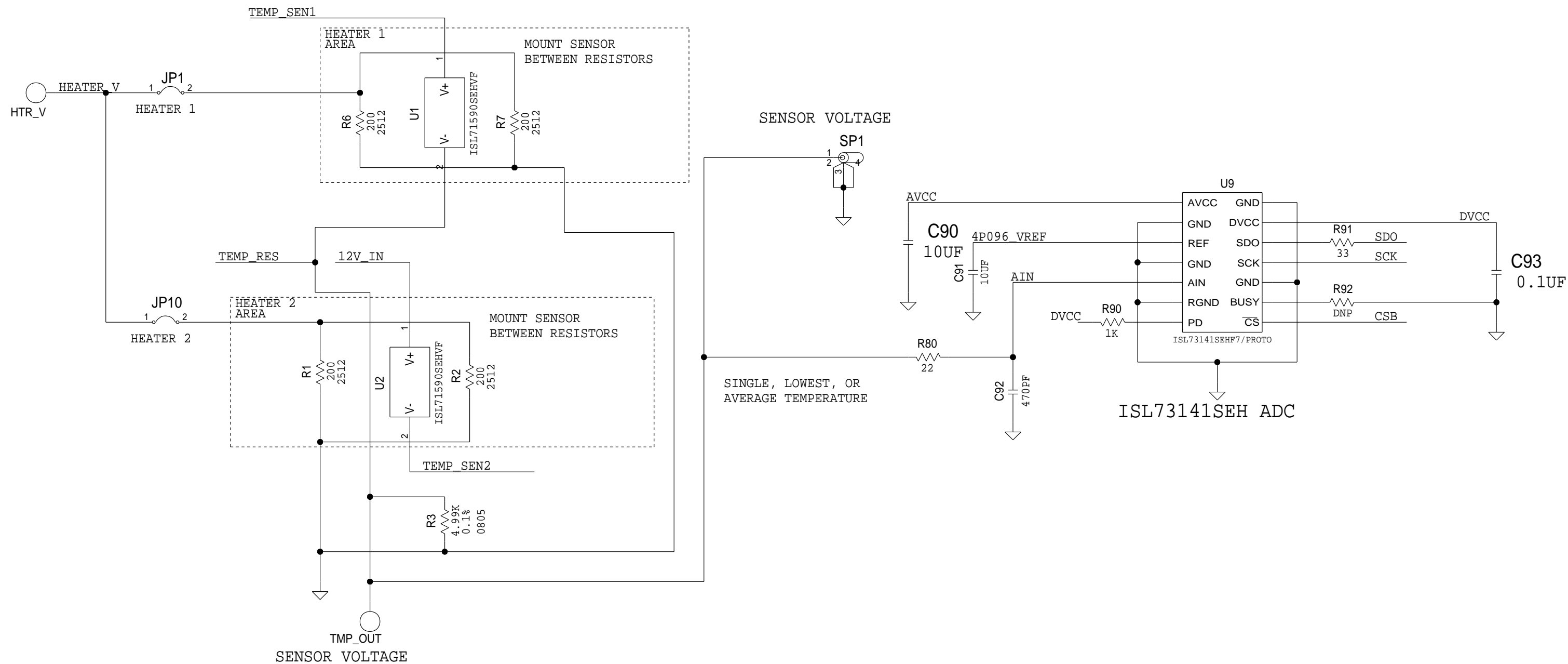
3.3V DVCC SUPPLY



4.096V REFERENCE

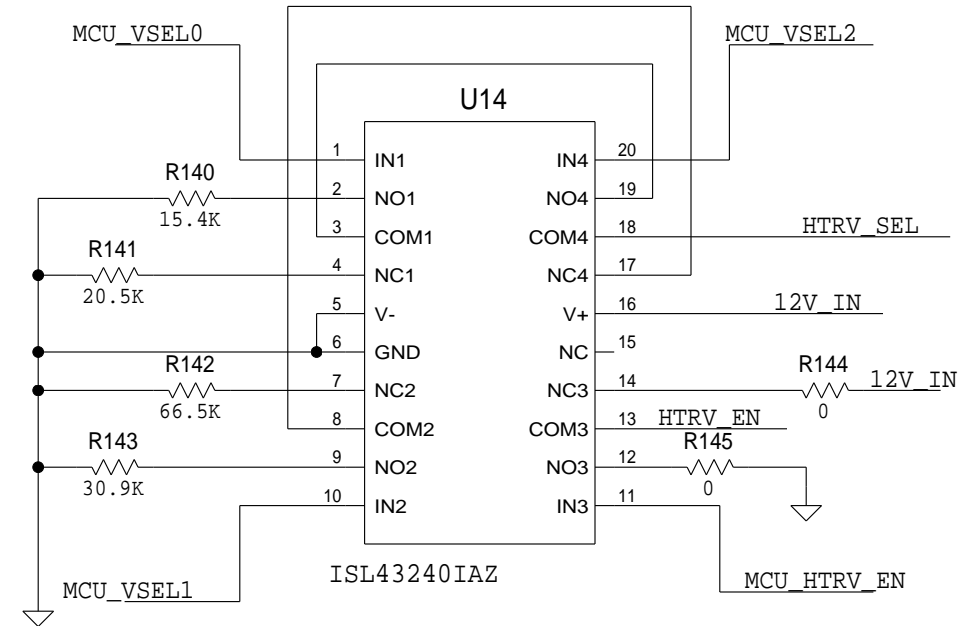
DRAWN BY: JONATHAN HARRIS	DATE: 06/16/2023	ENGINEER: JONATHAN HARRIS	DATE:
RELEASED BY:	DATE:	TITLE: ISL73141REFEV1Z	
UPDATED BY:	DATE:		
<b>intersil</b> <sup>™</sup>	TESTER	MASK#	HRDWR ID ISL73141REFEV1Z
		REV. B	
FILENAME: ISL73141REFEV1ZB.ZIP			SHEET 1 OF 4

# TEMP SENSOR CKT, DRIVER AMP, & ADC



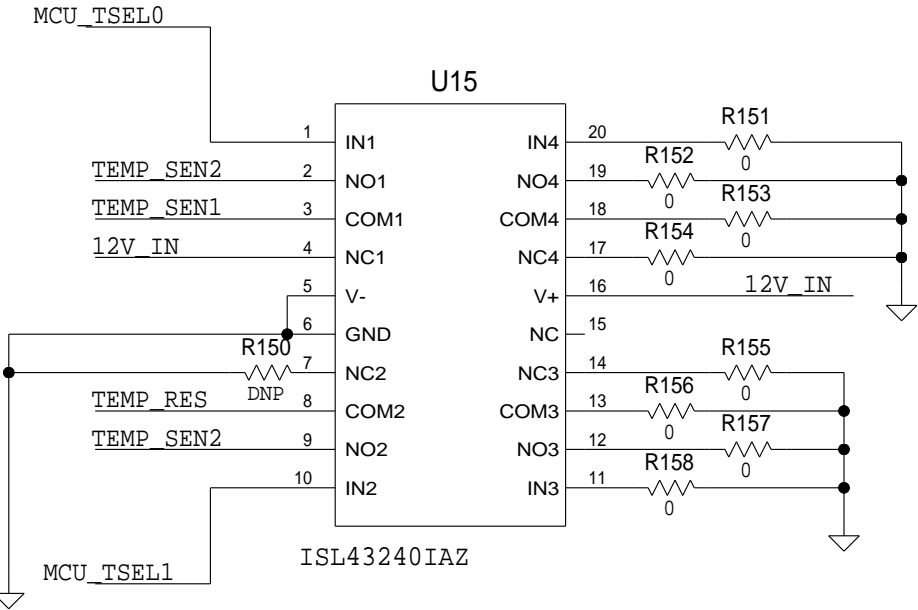
CONFIGURATION CIRCUITRY

HEATER VOLTAGE SELECTION AND ENABLE



V SELECT	MCU_VSEL(2:0)			
3.0V	0	0	0	0
3.0V	0	0	0	1
5.0V	0	1	0	0
5.0V	0	1	1	1
7.0V	1	0	0	0
9.0V	1	0	1	1
7.0V	1	1	1	0
9.0V	1	1	1	1

TEMP SENSOR CONFIGURATION CONTROL



CONFIG	MCU_TSEL (1:0)	
SINGLE	0	0
LOWEST	0	1
AVERAGE	1	0
UNUSED	1	1

# BOARD CONNECTORS

